

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Kazuhiko SHIRAKAWA Conf.:  
Appl. No.: NEW Group:  
Filed: August 1, 2003 Examiner:  
For: HETEROJUNCTION BIPOLAR TRANSISTOR AND  
METHOD FOR FABRICATING THE SAME

PRELIMINARY AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 1, 2003

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE SPECIFICATION

Please add the following line on page 1, please add the following line below the title "BACKGROUND OF INVENTION"

This application is a divisional of co-pending Application No. 09/641,819, filed on August 18, 2000, the entire contents of which are hereby incorporated by reference and for which priority is claimed under 35 U.S.C. § 120; and this application claims priority of Application No. 11-232671 filed in Japan on August 19, 1999 under 35 U.S.C. § 119.